Results for "((((lock<in>metadata)<and>(bit<in>metadata))<and>(read<in>metadata))<and>(write<in>metadata))"

✉ e-mail 🖶 printer

Your search matched **7** of **21** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» View Session History

» New Search

» Key

Modify Search

((((lock<in>metadata)<and>(bit<in>metadata))<and>(read<in>metadata))<and>(write [»]

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

☐ Check to search only within this results set

Display Format: ◉ Citation ○ Citation & Abstract

Select | **Article Information**

☐ 1. **Split FIFO phase synchronization for high speed interconnect**
Jex, J.; Nag, P.; Burton, T.; Mooney, R.;
Communications, Computers, and Signal Processing, 1995. Proceedings. IEEE Pacific Rim Con on
17-19 May 1995 Page(s):66 - 69
AbstractPlus | Full Text: PDF(344 KB)   IEEE CNF

☐ 2. **Clock jitter in a servo-derived clocking scheme for magnetic disk drives**
Yada, H.;
Magnetics, IEEE Transactions on
Volume 32, Issue 4, July 1996 Page(s):3283 - 3290
AbstractPlus | Full Text: PDF(672 KB)   IEEE JNL

☐ 3. **A high density magnetic tape cartridge storage system**
Daniels, L.; Kinsel, T.; Peterson, R.; Steinbrenner, R.; Wasser, D.; Williams, J.;
Magnetics, IEEE Transactions on
Volume 18, Issue 6, Nov 1982 Page(s):1227 - 1229
AbstractPlus | Full Text: PDF(440 KB)   IEEE JNL

☐ 4. **Proposal for a magnetic-film memory accessed by combined photon and electron beams**
Smith, D.;
Magnetics, IEEE Transactions on
Volume 3, Issue 4, Dec 1967 Page(s):593 - 599
AbstractPlus | Full Text: PDF(688 KB)   IEEE JNL

☐ 5. **Turning the tracking problem sideways: servo tricks for DVD+RW clock generation**
Abramovitch, D.;
American Control Conference, 2000. Proceedings of the 2000
Volume 4, 28-30 June 2000 Page(s):2615 - 2620 vol.4
AbstractPlus | Full Text: PDF(468 KB)   IEEE CNF

☐ 6. **A triple 8b, 80MSPS 3.3 V graphics digitizer**
Chaudrhry, I.A.; Kwak, S.-U.; Manganaro, G.; Sarraj, M.; Viswanathan, T.L.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on
Volume 5, 28-31 May 2000 Page(s):557 - 560 vol.5
AbstractPlus | Full Text: PDF(380 KB)   IEEE CNF

☐ 7. **Non-uniform access asynchronous register files**
Fang, D.; Manohar, A.;
Asynchronous Circuits and Systems, 2004. Proceedings. 10th International Symposium on
19-23 April 2004 Page(s):78 - 85
AbstractPlus | Full Text: PDF(1497 KB)   IEEE CNF

Welcome United States Patent and Trademark Office

ⵏ‏Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE   SUPPOF

Results for "(read<in>metadata) <and> (write<in>metadata) <and> (lock<in>metadata)"
Your search matched **59** of **1152881** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

✉ e-mail   🖶 printer

» View Session History

» New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

(read<in>metadata) <and> (write<in>metadata) <and> (lock<in>metadata)   ≫

☐ Check to search only within this results set

Display Format: ⦿ Citation   ◌ Citation & Abstract

Select   Article Information

View: **1-25** | 26-5(

☐ 1. **Locking with different granularities for reads and writes in an MVM system**
Bodorik, P.; Jutla, D.; Slonim, J.; Agarwal, A.;
Database Engineering and Applications, 1999. IDEAS '99. International Symposium Proceedings
2-4 Aug. 1999 Page(s):311 - 320

AbstractPlus | Full Text: PDF(100 KB)   IEEE CNF

☐ 2. **Split FIFO phase synchronization for high speed interconnect**
Jex, J.; Nag, P.; Burton, T.; Mooney, R.;
Communications, Computers, and Signal Processing, 1995. Proceedings. IEEE Pacific Rim Con
on
17-19 May 1995 Page(s):66 - 69

AbstractPlus | Full Text: PDF(344 KB)   IEEE CNF

☐ 3. **User defined prewrites for increasing concurrency in design databases**
Madria, S.K.; Embong, A.;
Information, Communications and Signal Processing, 1997. ICICS., Proceedings of 1997 Interna
Conference on
9-12 Sept. 1997 Page(s):810 - 814 vol.2

AbstractPlus | Full Text: PDF(372 KB)   IEEE CNF

☐ 4. **A model for database allocation incorporating a concurrency control mechanism**
Ram, S.; Marsten, R.E.;
Knowledge and Data Engineering, IEEE Transactions on
Volume 3, Issue 3, Sept. 1991 Page(s):389 - 395

AbstractPlus | Full Text: PDF(608 KB)   IEEE JNL

☐ 5. **Clarification of two phase locking in concurrent transaction processing**
Leu, P.-J.; Bhargava, B.;
Software Engineering, IEEE Transactions on
Volume 14, Issue 1, Jan. 1988 Page(s):122 - 125

AbstractPlus | Full Text: PDF(320 KB)   IEEE JNL

☐ 6. **A VLSI chip set for burst and fast ATM switching**
Fried, J.;
Communications, 1989. ICC 89, BOSTONICC/89. Conference record. World Prosperity Through
Communications, IEEE International Conference on
11-14 June 1989 Page(s):128 - 135 vol.1

AbstractPlus | Full Text: PDF(540 KB)   IEEE CNF

☐ 7. **A 32 Mb/s disk drive data separator for constant density recording**
Horita, R.; Miyazawa, S.; Hase, K.; Hirano, A.; Kojima, S.;
Custom Integrated Circuits Conference, 1991., Proceedings of the IEEE 1991
12-15 May 1991 Page(s):9.6/1 - 9.6/4

AbstractPlus | Full Text: PDF(396 KB)   IEEE CNF

8. **Register locking in an asynchronous microprocessor**
Paver, N.C.; Day, P.; Furber, S.B.; Garside, J.D.; Woods, J.V.;
Computer Design: VLSI in Computers and Processors, 1992. ICCD '92. Proceedings., IEEE 199:
International Conference on
11-14 Oct. 1992 Page(s):351 - 355
AbstractPlus | Full Text: PDF(356 KB)   IEEE CNF

9. **A high speed digital data separator design using real time DSP for disk drive applications**
Kim, B.; Greco, J.D.; Helman, D.N.; Ngo, H.; Yang, H.C.; Wu, W.-C.S.; Chowdhury, R.F.;
Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE International Symposium on
Volume 2, 3-6 May 1992 Page(s):625 - 628 vol.2
AbstractPlus | Full Text: PDF(308 KB)   IEEE CNF

10. **Optimizing a FIFO, scalable spin lock using consistent memory**
Rhee, I.;
Real-Time Systems Symposium, 1996., 17th IEEE
4-6 Dec. 1996 Page(s):106 - 114
AbstractPlus | Full Text: PDF(696 KB)   IEEE CNF

11. **Multi-dimensional locks with on-line allocation schemes**
Scheerer, D.; Schenfeld, E.; Viredaz, M.A.;
Distributed Computing Systems, 1996., Proceedings of the 16th International Conference on
27-30 May 1996 Page(s):241 - 248
AbstractPlus | Full Text: PDF(648 KB)   IEEE CNF

12. **Hybrid concurrency control for mobile computing**
Sung Ho Cho; JongMin Lee; Chong-Sun Hwang; WonGyu Lee;
High Performance Computing on the Information Superhighway, 1997. HPC Asia '97
28 April-2 May 1997 Page(s):478 - 483
AbstractPlus | Full Text: PDF(388 KB)   IEEE CNF

13. **Object replication using version vector**
Hasegawa, K.; Higaki, H.; Takizawa, M.;
Parallel and Distributed Systems, 1998. Proceedings., 1998 International Conference on
14-16 Dec. 1998 Page(s):147 - 154
AbstractPlus | Full Text: PDF(196 KB)   IEEE CNF

14. **Object-based locking protocol for replicated objects**
Hasegawa, K.; Takizawa, M.;
Information Networking, 1998. (ICOIN-12) Proceedings., Twelfth International Conference on
21-23 Jan. 1998 Page(s):398 - 401
AbstractPlus | Full Text: PDF(80 KB)   IEEE CNF

15. **Non-blocking data sharing in multiprocessor real-time systems**
Tsigas, P.; Yi Zhang;
Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth International Conferen
13-15 Dec. 1999 Page(s):247 - 254
AbstractPlus | Full Text: PDF(544 KB)   IEEE CNF

16. **An adaptable constrained locking protocol for high data contention environments**
Goel, S.; Bhargava, B.; Sanjay Kumar Madria;
Database Systems for Advanced Applications, 1999. Proceedings., 6th International Conference
19-21 April 1999 Page(s):321 - 328
AbstractPlus | Full Text: PDF(100 KB)   IEEE CNF

17. **Turning the tracking problem sideways: servo tricks for DVD+RW clock generation**
Abramovitch, D.;
American Control Conference, 2000. Proceedings of the 2000
Volume 4, 28-30 June 2000 Page(s):2615 - 2620 vol.4
AbstractPlus | Full Text: PDF(468 KB)   IEEE CNF

18. **Transactions on partially replicated data based on reliable and atomic multicasts**
Fritzke, U., Jr.; Ingels, P.;

Distributed Computing Systems, 2001. 21st International Conference on.
16-19 April 2001 Page(s):284 - 291

AbstractPlus | Full Text: PDF(664 KB)   IEEE CNF

19. **A 160-MHz analog front-end IC for EPR-IV PRML magnetic storage read channels**
Pai, P.K.D.; Brewster, A.D.; Abidi, A.A.;
Solid-State Circuits, IEEE Journal of
Volume 31, Issue 11, Nov. 1996 Page(s):1803 - 1816

AbstractPlus | References | Full Text: PDF(1368 KB)   IEEE JNL

20. **Clock jitter in a servo-derived clocking scheme for magnetic disk drives**
Yada, H.;
Magnetics, IEEE Transactions on
Volume 32, Issue 4, July 1996 Page(s):3283 - 3290

AbstractPlus | Full Text: PDF(672 KB)   IEEE JNL

21. **Maintaining temporal consistency: pessimistic vs. optimistic concurrency control**
Xiaohui Song; Liu, J.W.S.;
Knowledge and Data Engineering, IEEE Transactions on
Volume 7, Issue 5, Oct. 1995 Page(s):786 - 796

AbstractPlus | References | Full Text: PDF(1132 KB)   IEEE JNL   .

22. **Modular synchronization in distributed, multiversion databases: version control and concurrency control**
Agrawal, D.; Sengupta, S.;
Knowledge and Data Engineering, IEEE Transactions on
Volume 5, Issue 1, Feb. 1993 Page(s):126 - 137

AbstractPlus | Full Text: PDF(1208 KB)   IEEE JNL

23. **Lock-free garbage collection for multiprocessors**
Herlihy, M.P.; Moss, J.E.B.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 3, Issue 3, May 1992 Page(s):304 - 311

AbstractPlus | Full Text: PDF(732 KB)   IEEE JNL

24. **Oscillation modes of laser diode pumped hybrid bistable system with large delay and app to dynamical memory**
Aida, T.; Davis, P.;
Quantum Electronics, IEEE Journal of
Volume 28, Issue 3, March 1992 Page(s):686 - 699

AbstractPlus | Full Text: PDF(1240 KB)   IEEE JNL

25. **An optimistic locking technique for concurrency control in distributed databases**
Halici, U.; Dogac, A.;
Software Engineering, IEEE Transactions on
Volume 17, Issue 7, July 1991 Page(s):712 - 724

AbstractPlus | Full Text: PDF(984 KB)   IEEE JNL

Indexed by
Inspec

IEEE *Xplore* ℗
RELEASE 2.0

⊞ AbstractPlus

◀ View Search Results

Document options

📄 Full Text: PDF (454 KB)

Download this citation

Choose | Citation ▾ |

Download | EndNote,ProCite,RefMan ▾ |

» Learn More

Rights & Permissions

Request Permissions
RIGHTSLINK ‹›

» Learn More

Distributed-directory scheme: scalable coherent interface

James, D.V.   Laundrie, A.T.   Gjessing, S.   Sohi, G.S.
Apple Comput., New York, NY, USA;

Abstract
The scalable coherent interface (SCI), a local or extended computer backplane interface being defir
IEEE standard project (P1596), is discussed. the interconnection is scalable, meaning that up to 64
processor, memory, or I/O nodes can effectively interface to a shared SCI interconnection. The SCI
structures are described, and sharing-list addition and removal are examined. Optimizations being c
to improve the performance of large system configurations are discussed. Request combining, a us
of linked-list coherence, is described. SCI's optional extensions, including synchronization using a q
lock bit, are considered

Index Terms
Inspec
　　　　Controlled Indexing
　　　　computer interfaces   data structures

　　　　Non-controlled Indexing
　　　　IEEE standard project   P1596   SCI sharing-list structures   computer backplane interface
　　　　distributed directory scheme   large system configurations   linked-list coherence   queued-c
　　　　lock bit   scalable coherent interface
Author Keywords
Not Available

References

No references available on IEEE Xplore.

Citing Documents

1   ADir$_p$NB: a cost-effective way to implement full map directory-based cache coherence protocols
　　John, L.K.
　　Computers, IEEE Transactions on
　　On page(s): 921-934, Volume: 50, Issue: 9, Sep 2001
　　Abstract | Full Text: PDF (1312)

2   An efficient tree cache coherence protocol for distributed shared memory multiprocessors, Yeim
　　Chang; Bhuyan, L.N.
　　Computers, IEEE Transactions on
　　On page(s): 352-360, Volume: 48, Issue: 3, Mar 1999
　　Abstract | Full Text: PDF (404)

◀ View Search Results

Indexed by
📕 Inspec*